



IFW

Docket No.: M4065.0656/P656  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Terry L. Gilton

Application No.: 10/663,741

Confirmation No.: 2658

Filed: September 17, 2003

Art Unit: 2811

For: NON-VOLATILE MEMORY STRUCTURE

Examiner: C. Q. Nguyen

**RESPONSE TO RESTRICTION REQUIREMENT**

MS Amendment  
Commissioner for Patents  
220 20th Street, S.  
Customer Window  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, VA 22202

Dear Sir:

The Office Action mailed June 24, 2004 has required restriction between:

Group I. Claims 1-9, 26-46, drawn to a semiconductor device; and

Group II. Claims 10-25, drawn to a method of making a semiconductor device.

In response, Applicant hereby elects Group I, claims 1-9 and 26-46 for continued examination, without traverse.

Application No.: 10/663,741

Docket No.: M4065.0656/P656

Dated: June 30, 2004

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Christopher S. Chow

Registration No.: 46,493

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant